

Registration Form

**One Day Seminar
on
RECENT ADVANCES IN ELECTRONICS
ENGINEERING**
(Under TEQIP Phase-II)
(1st October 2016)

1. Name:
2. Gender:
3. Designation:
- Department:.....
4. Organization:.....
5. Correspondence address:
- Mobile. No.:.....
- E-mail:
6. Qualifications:
7. Teaching/Research Exp.
(Years):.....
8. Lodging Requirement: (Chargeable)
Yes/No.....
9. Area of Research:
10. Registration fee details:
 - (i) Amount:
 - (ii) Demand Draft details:
Date:
 - Number:
 - Issuing Bank:

(Signature of the Applicant with date)

(Signature & Seal of Head of
Department/Institute with date)

Note: Photocopy of the form can be used.

Organizing Committee

Patron Er. Ramesh Agrawal	Secretary, Scindia Engg. College Society
Chairman Dr. R. K. Pandit	Director
General Chair Dr. P. K. Singhal	Professor & Head, EED
Convenor Dr. Bimal Garg	Associate Professor, EED
Coordinators Dr. L. Shrivastava Dr. R. P. Narwaria	Assistant Professor, EED Assistant Professor, EED
Organizing Secretary Prof. Madhav Singh Prof. Deep K. Parsediya	Assistant Professor, EED Assistant Professor, EED
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Dr. M. Pandit	Dean Academic
Dr. Rajeev Kansal	Dean Student Welfare
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Dr. L. Srivastava	Head, Electrical Engineering
Dr. A. Tiwari	Head, Civil Engineering
Dr. Alok Sharma	Head, Architecture
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Dr. M. Pandit	Head, Biotechnology
Dr. Abhay Mishra	Head, Applied Sciences
Prof. S Prashad	Head, Chemical Engineering
Dr. M. K. Gaur	Head, Mechanical Engineering
Prof. Manish Dixit	Coordinator, IT

Contact

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One Day Seminar on RECENT ADVANCES IN ELECTRONICS ENGINEERING

(Under TEQIP Phase-II)
(1st October 2016)



**Department of Electronics
Engineering**
Madhav Institute of Technology & Science
(An Autonomous Government Aided Institute)
Race Course Road, Gole ka Mandir,
Gwalior-474005 www.mitsgwalior.in

About the Institute

Madhav Institute of Technology & Science, Gwalior was established in 1957 by His Highness Late Sir Jiwaji Rao Scindia, Maharaja, of the erstwhile state of Gwalior. The foundation stone of the Institute was laid by Late Dr. Rajendra Prasad, on 20th October, 1956 and the building was inaugurated by Late Dr. S. Radhakrishnan on 11th December, 1964. The president of India Dr. Pratibha Devi Singh Patil graced the Golden Jubilee Celebrations of the institute as Chief Guest on 30th June, 2008.

The institute has a campus of around 45 acres. The Institute offers education in nine undergraduate programmes and fifteen PG programmes. The prime objective of the Institute is to provide quality technical education at undergraduate and postgraduate levels. Recently the Institute is also funded by World Bank under TEQIP phase II to strengthen the quality of technical education.

About the Department

The department of Electronics Engineering was established in 1981. The department offers two UG and two PG programmes. The faculty is actively engaged in research, and has published numerous papers in National and International Journals. Department has the facility of various technical and core laboratories for students. The alumni of the department have secured their places in the higher echelons of the society and technical world.

Aim & Scope the Seminar

One day seminar on "Recent Advances in Electronics Engineering" under TEQIP-II' is scheduled to be held during 1st October, 2016. The main objective of this seminar is to promote the recent advances in the field of Electronics Engineering. Electronics is an engineering discipline which needs immediate attention of the society for the manpower that would be needed in the future. The aim of the program is to create awareness about the state of the art in electronics. An ample opportunity that it would provide for the next generation engineers. The latest upcoming technologies will also be discussed by the expertise in the seminar.

We would like to invite all scholars and experts to attend the seminar to be hosted at beautiful historical city Gwalior. The seminar will include regular sessions on the topics listed below and some special sessions on emerging topics.

Topics to be Covered

- Electronics Engineering
- Electronics devices & Circuit.
- Recent advancement in Electronics
- Solid state devices
- Latest upcoming Technology
- Analog Circuits.
- VLSI & MEMS.
- Low noise devices and techniques
- High power devices and techniques
- Smart antenna and arrays.
- Embedded System
- Solar Energy
- Nano technology in processing of materials

Registration

This Seminar is open to research scholars, faculty members and industry personals.

Registration Fee: Research Scholar: Rs. 200
Faculty Members: Rs. 400
Industry Personnel: Rs. 600

How to Register

The participants may register by sending the completed and duly signed 'Registration Form' along with the Demand Draft in favor of 'Director, MITS, Gwalior' payable at Gwalior by 28th September 2016 through post or email to event.ec.mits2017@gmail.com Those who will send scanned copy of DD should come with original DD at the time of registration. This workshop is open to research scholars, faculty members and industry personals.

Resource Person

Experts will be from reputed institutes such as NITs and from esteemed industries for proper blend of academia and industry.

Important Instructions

Last date to intimate the confirmation of the participation through e-mail is 29th September, 2016. Please note that confirmation email is send to registered email; please check your email regularly. Travel and accommodation expenses have to be borne by participants themselves. Limited accommodation for outside participants may be provided in institute Hostel/ Hotel on prior information and on payment basis.