REGISTRATION FORM

One Day National Workshop
On
Recent Advances in
Communication Engineering
Under TEQIP Phase-II
(February 17, 2017)

1.Name:..

2. Gender:..

3. Designation:...

4. Organization:...

5. Correspondence address:.................................

....

   Mobile. No.:...

   E-mail:..

6. Qualifications: ....

7. Teaching/Research Exp.(years):........................

8. Lodging Requirement: Yes/No...........................
   (Chargeable)

9. Area of Research: ....

....

10. Registration fee details:

   (i) Amount:...

   (ii) Demand Draft details:
      a)   Date: ....
      b)   Number: ....
      c)   Issuing Bank: ....

(Signature of the Applicant with date)

(Signature of Head of Department/Institute with date)

Note: Photocopies of this form can be used for registration.

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Patron

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Chairman

Dr. R. K. Pandit — Director

Convenor

Dr. S.S. Bhadauria — Professor & Head, Department of Electronics Engineering, TEQIP II Coordinator

Coordinators

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Dr. Karuna Markam — Assistant Professor, ECD

Organizing Secretary

Prof. Madhav Singh — Assistant Professor, ECD
Prof. Pooja Sahoo — Assistant Professor, ECD

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Dr Bimal Garg	Dr. R. P. Narwaria
Dr Rekha Gupta	Prof. Deep Parsedia

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ADDRESS FOR COMMUNICATION

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Madhav Institute of Technology and Science

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ORGANISED BY
Department of Electronics Engineering
Madhav Institute of Technology &
Science, Gwalior-474005
(An Autonomous Government Aided Institute)
www.mitsgwalior.in

ABOUT THE INSTITUTE

Madhav Institute of Technology & Science, Gwalior was established in 1957 by His Highness Late Sir Jiwaji Rao Scindia, Maharaja, of the erstwhile state of Gwalior. The foundation stone of the Institute was laid by Late Dr. Rajendra Prasad, on 20th October, 1956 and the building was inaugurated by Late Dr. S. Radhakrishnan on 11th December, 1964. The president of India, Dr. Pratibha Devi Singh Patil graced the Golden Jubilee Celebrations of the institute as Chief Guest on 30th June, 2008.

The institute has a campus of around 45 acres. The Institute offers education in nine undergraduate programmes and fifteen PG programmes. The prime objective of the Institute is to provide quality technical education at undergraduate and postgraduate levels. Recently the Institute is also funded by World Bank under TEQIP phase II to strengthen the quality of technical education.

ABOUT THE DEPARTMENT

The department of Electronics Engineering was established in 1981. The department offers two UG with the intake of 120 students in Electronics Engineering and intake of 60 in Electronics & Telecommunication Engineering, also two PG programmes with the intake of 18 students in M.Tech Microwave and intake of 25 in M.E. CCN. The faculty members are actively engaged in research in the area of Adhoc Networking, Image Processing, Wireless Communication, Microwave, and Antenna Theory. They have published numerous papers in National and International Journals and conferences. The alumni of the department have secured their places in the higher echelons of the society and technical world. A good number of students are going for higher studies for their M.Tech, MBA and PhD. from different reputed institutes in India.

OBJECTIVES OF THE COURSE

The Objective of the workshop is to train the Faculty, Research Scholars and PG Students of Engineering colleges for their research work in the fields of Communications and Signal Processing. Communication Engineering is indispensable technologies that have formed the bedrock of the modern wireless communication technology, which have now permeated through many aspects of our lives. The aim of this workshop is to impact the knowledge and skills in the area of Communication engineering. The course content of workshop will include

Course Content:
1. Digital Image Processing.
2. Digital Communication
3. Communication Control & Networking
4. Microwave Engineering
5. VLSI Design
6. Biomedical Engineering
7. Information Security
8. Nanotechnology
9. Optical Communication
10. Satellite Communication
11. Wireless Communication
12. Data Communication
13. Digital Signal Processing
14. Neural Networks

ELIGIBILITY FOR PARTICIPANTS

- Teaching faculty from AICTE approved Engineering Colleges
- Researchers and PG students from AICTE Approved Engineering Colleges

REGISTRATION

Registration Fee: No Registration fees

How to Register

The participants may register by sending the completed and duly signed 'Registration Form' by post or E-mail to the department. Seats are limited to 50 and application form will be selected on first-cum-first basis.

Accommodation

Chargeable accommodation arrangement can be made on prior request to us before 16 February 2017.

ROUTE TO MITS, GWALIOR